

Docket No. 201163US-2S  
IN RE APPLICATION OF: JUNICHI ASADA  
SERIAL NO: 09/740,902  
FILED: DECEMBER 21, 2000  
FOR: SEMICONDUCTOR DEVICE

\*RESPONSE UNDER 37 CFR 1.116-  
EXPEDITED PROCEDURE EXAMINING  
GROUP 2815

RECEIVED  
JUL - 8 2002  
TECHNOLOGY CENTER 2800

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Transmitted herewith is an amendment in the above-identified application.

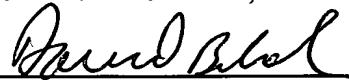
- No additional fee is required
- Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- Additional documents filed herewith: Marked-Up Copy

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	2	MINUS	20	0	× \$18 =	\$0.00
INDEPENDENT	2	MINUS	3	0	× \$84 =	\$0.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS		+ \$280 =		\$0.00
						\$0.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$0.00
		<input type="checkbox"/> Recordation of Assignment		+ \$40 =		\$0.00
						TOTAL \$0.00

- A check in the amount of \_\_\_\_\_ is attached.
- Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.



Gregory J. Maier  
Registration No. 25,599

David A. Bilodeau  
Registration No. 42,325



**22850**

Tel. (703) 413-3000  
Fax. (703) 413-2220  
(OSMMN 10/00)

I:\atty\Rff\201163\201163.amcvr.wpd

201163US-620-39-2S

AMENDMENT UNDER 37 C.F.R. §1.116  
EXPEDITED PROCEDURE EXAMINING GROUP

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

JUNICHI ASADA

: EXAMINER: CHU, C. C.

SERIAL NO: 09/740,902

: GROUP ART UNIT: 2815

FILED: DECEMBER 21, 2000

FOR: SEMICONDUCTOR DEVICE

#10/13  
Am dt  
Htg  
J. McMillan  
7/17/02  
RECEIVED  
JUL - 8 2002  
TECHNOLOGY CENTER 2800

AMENDMENT UNDER 37 C.F.R. §1.116

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

In response to the Office Action dated April 11, 2002, please amend this application  
as follows:

Entered  
J. McMillan  
8/20/02

Do not enter  
C-C.

7/18/02

IN THE CLAIMS

Please cancel Claims 1-3, 5, 6, 9, 11 and 13 without prejudice.

Please add new Claims 28 and 29 as shown below:

28. (New) A semiconductor memory device comprising:

a semiconductor element;

a plurality of lead wires connected to a plurality of connecting electrodes formed on  
said semiconductor element;

at least a single dummy lead wire that is not electrically connected to said  
semiconductor element and does not include an outer lead portion for electrically connecting

SUB  
C1

b